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How To Crack Full version ROUSEAU MAROC GAME – Episode 2 DOWNLOAD by ARMAN1. Field of the Invention The present invention relates to a semiconductor integrated circuit blocks, and more particularly to a semiconductor integrated circuit having protective elements against surge and noise for the circuit blocks. 2. Description of the Related Art An integrated circuit such as a microprocessor or a microcontroller has circuit blocks such as memory circuits, arithmetic logic circuits, or control circuits to achieve its function, which are arranged on a chip (semiconductor substrate). These circuit blocks have circuit elements such as transistors, which may be destroyed by various noises. Particularly, internal circuits disposed in a peripheral circuit area (also called internal core) where a number of transistors are concentrated receive noise having a large influence on the operating characteristic of the circuits. In particular, if the noise wave is large or the noise level is high, the resistors, transistors, and the like constituting the circuits easily cause destruction, and thus the semiconductor integrated circuit cannot normally operate. Especially, when a large number of transistors are mounted on a single chip, it is difficult to remove a noise wave having a wide band from this large number of transistors. To cope with this, there have been proposed various techniques for protecting the transistors in the internal circuit against destruction by a noise. In these protective techniques, an example using PN junction diodes, another example using MOS FETs, and the like are widely known. However, these examples have a drawback in that a large number of transistors will not span multiple lines and would just wrap. However,



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